S-19 June & 6 July 2012 AC after Circulars from Circular No.84 & onwards  - 89 -

DR. BABASAHEB AMBEDKAR MARATHWADA UNIVERSITY

CIRCULAR NO. ACAD / NP /S.E./Elect. Engg./Syllabi/125/2012

In continuation of this Office CIRCULAR NO. ACAD / NP /S.E./B.E./Syllabi/88/2012 dated 31-07-2012, circulated the syllabi of S.E. in all Branches of Engineering, but due to oversight in the syllabus of S.E. Electrical Engineering/EEP/EE / EEE, the portion of Paper Number BSH-201:Engineering Mathematics-III & BSH-251:Engineering Mathematics-IV are wrongly produced. Now the said paper’s have been rectified and enclosed with the syllabus.

The Hon’ble Vice-Chancellor has given approval to the said modified syllabus in his emergency powers on behalf of the Academic Council Under Section-14(7) of the Maharashtra Universities Act, 1994 as appended herewith under the Faculty of Engineering & Technology.

This is effective from the academic year 2012-2013 and onwards.

All concerned are requested to note the contents of this circular for their information and necessary action.

University Campus,
Aurangabad-431 004.

REF. NO. ACAD/ NP / S.E. ELECTR.
ENGG/2012/3298-33011

Date: 08-10-2012.

Direcor,
Board of College and
University Development.

Copy forwarded with compliments to :-

1) The Principals, affiliated concerned Colleges,
Dr. Babasaheb Ambedkar Marathwada University.

Copy to :-

1) The Controller of Examinations,
2) The Superintendent, [Engineering Unit],
3) The Superintendent, [Eligibility Unit],
4) The Record Keeper,
Dr. Babasaheb Ambedkar Marathwada University.

S*/081012/-
Dr BABASAHEB AMBEDKAR
MARATHWADA UNIVERSITY,
AURANGABAD

Revised Structure and Syllabus of
Second Year Engineering of

ELECTRICAL
ENGINEERING/EAP/EE/EEE

EFFECTIVE FROM - 2012-13 & ONWARDS
RULES AND REGULATIONS.

FOR

SECOND YEAR DEGREE COURSE IN ENGINEERING (REVISED)

(Applicable from the Academic Year 2012-2013)

Note:
1. All the Rules and Regulations, hereinafter specified shall be read as a whole for the purpose of interpretation.

ADMISSION
1. Admission to second year engineering shall be carried out as per the rules and regulations prescribed by the competent authority as appointed by the Government of Maharashtra and Dr. Babasaheb Ambedkar Marathwada University, Aurangabad, from time to time.

DURATION AND COURSES OF STUDY
1. The duration of the course is four years. Each of the four academic years shall be divided into two semesters herein after referred to as the semester I and semester II in chronological order. Each semester shall comprise

- Instructions .................. 15 weeks
- Preparation holiday ............... 2 weeks or 15 days
  (Includes practical exams)

2. Candidate who fails to fulfill all the requirements for the award of the degree as specified hereinafter within eight academic years from the time of admission, will forfeit his/her seat in the course and his/her admission will stand cancelled.

RULES AND REGULATION OF ATTENDANCE
1. Candidates admitted to a particular course of study are required to pursue a “Regular course of study” as prescribed by the University before they are permitted to appear for the University Examination.

2. “A regular course of study” means putting in attendance not less than 75% for individual subject.

3. a) In special cases and for sufficient causes shown, the Principal of the institute may, on the specific recommendation the Head of the Department, condone the deficiency in attendance to the extent of 15% on medical ground subject to submission of medical certificate.

   b) However, in respect of women candidates who seek condonation of attendance due to pregnancy, the Principal may condone the deficiency in attendance to the extent of 25% (as against 15% Condonation for other) on medical grounds subject to submission of medical certificate to this effect. Such condonation be availed twice during the entire course of study leading to degree in Engineering and Technology.
4. "Active Participation in N.C.C/N.S.S. Camps or Inter collegiate or Inter University or Inter State or International matches or debates of Educational Excursions or such other Inter University activities as approved by the authorities involving journeys outside the city in which the college is situated will not be counted as absence. However, such absence shall not exceed (4) weeks per semester of the total period of instructions. Such leave should not be availed more than twice during the entire course of study.

5. The attendance shall be calculated on individual papers/subjects from the date of commencement of the semester.

6. In case of the candidates who fail to put in the required attendance in a course of study, he/she shall be detained in the same class and will not be recommended to appear for the University examination.

7. A candidate detained in semester I should take readmission in next academic year as a regular student and shall have to complete all the theory and practicals as a regular student.

8. In case a candidate is detained in semester II, he/she should take admission in Semester II of next academic year and complete all the theory and practicals as a regular student of semester II.

9. In case of change of syllabus the candidate even if detained in semester II should take readmission in next academic year for Semester I and II as a regular student and complete all the theory and practicals as a regular student.

**SCHEME OF INSTRUCTIONS AND EXAMINATION**

1. Instructions about the curriculum in the various subjects in each semester of all the four years shall be provided by the University.

2. The details of instruction period, examination schedule, vacations etc. shall be notified by the Principal of the College as per the University academic calendar.

3. The medium of instruction and examination shall be English.

4. At the end of each semester, University examinations shall be held as prescribed in the respective schemes of examination.
5. The examinations prescribed may include written papers, practical and oral tests, inspection of certified sessional work in Drawing and Laboratories and work done by students in each practical examination, along with other materials prepared or collected as part of Lab work/Project.

6. All the rules for examinations prescribed by the University from time to time shall be adhered to.

7. A candidate shall be deemed to have fully passed the Examination of a semester, if he/she secures not less than the minimum marks/grade as prescribed.

8. Institutions will be encouraged to adopt modern tools in classroom/labs to deliver the course contents.

9. Institutions will be encouraged to conduct online class tests.

0.874

The Second Year Examination in Engineering will be held in two parts S.E. semester-I and S.E. semester-II. No candidate will be admitted to S.E. semester-I examination unless he/she produce testimonials of having kept one term, for the subject under F.E. semester-I and II satisfactorily in a college of engineering affiliated to this University after passing the First year examination of engineering other examination recognized as equivalent thereto as per the admission rules to second year engineering prescribed by the Government of Maharashtra and Dr. B.A.M.University from time to time.
### Structure of syllabus of subject

<table>
<thead>
<tr>
<th>Code No:</th>
<th>Title:</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>Examination Scheme</td>
</tr>
<tr>
<td></td>
<td>Class Test: Marks</td>
</tr>
</tbody>
</table>

**Teaching Scheme**

<table>
<thead>
<tr>
<th>Theory: hours/week</th>
<th>Class Test: Marks</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>Theory examination: Maximum hours</td>
</tr>
<tr>
<td></td>
<td>Theory examination: Maximum Marks</td>
</tr>
<tr>
<td></td>
<td>Practical/ Oral examination: Maximum Marks</td>
</tr>
</tbody>
</table>

**Tutorial: hours/week**

**Practical/ TermWork : hours/week**

**Objectives:**

1.
2.
3.

**Unit 1:**

**Unit 2:**

**Unit 3:**

**Unit 4:**

**Unit 5:**

**Unit 6:**

**Text Books:**

1.
2.

**Reference Books:**

1.
2.
3.
4.

### Pattern of Question Paper:

The six units in the syllabus shall be divided in two equal parts i.e. 3 units respectively. Question paper shall be set having two sections A and B. Section A questions shall be set on first part and Section B questions on second part. Question paper should cover the entire syllabus.

**For 80 marks Paper:**

1. Minimum ten questions
2. Five questions in each section
3. Question no 1 from section A and Question no 6 from section B be made compulsory and should have at least eight bits of two marks out of which five to be solved
4. Two questions from remaining questions from each section A and B be asked to solve having weightage of 15 marks

**For 40 marks Paper:**

1. Minimum eight questions
2. Four questions in each section
3. Question no 1 from section A and Question no 5 from section B be made compulsory and should have at least five bits of two marks out of which three to be solved.

4. Two questions from remaining questions from each section be asked to solve having weightage of 7 marks.

**0.95 GRACE MARKS FOR PASSING IN EACH HEAD OF PASSING (THEORY / PRACTICAL / ORAL / SESSIONAL) (EXTERNAL / INTERNAL)**

The examinee shall be given the benefit of grace marks only for passing in each head of passing (Theory/practical/Oral/Sessional) in external or internal examination as follows:

<table>
<thead>
<tr>
<th>Head of passing</th>
<th>Grace Marks upto</th>
</tr>
</thead>
<tbody>
<tr>
<td>Up to 50</td>
<td>2</td>
</tr>
<tr>
<td>051 to 100</td>
<td>3</td>
</tr>
<tr>
<td>101 to 150</td>
<td>4</td>
</tr>
<tr>
<td>151 to 200</td>
<td>5</td>
</tr>
<tr>
<td>201 to 250</td>
<td>6</td>
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<tr>
<td>251 to 300</td>
<td>7</td>
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<tr>
<td>301 to 350</td>
<td>8</td>
</tr>
<tr>
<td>351 to 400</td>
<td>9</td>
</tr>
<tr>
<td>And 401 and above</td>
<td>10</td>
</tr>
</tbody>
</table>

Provided that the benefit of such grading marks given in different heads of passing shall not exceed 0.1 percent of the aggregate marks in that examination.

Provided, further, that the benefit of grading of marks under this Ordinance shall be applicable only if the candidate passes the entire examination of semester/year.

Provided further that this grading is concurrent with the rules and guidelines of professional statutory bodies at the All India level such as AICTE, MCI, Bar Council, CCIM, CCIH, NCTE, UGC etc.

**0.96 GRACE MARKS FOR GETTING HIGHER CLASS**

A candidate who passes in all the subjects and heads of passing in the examination without the benefit of either grading is condonation rules and whose total number of marks falls short for securing Second Class/Honours Second class of First Class by marks not more than 0.1 percent of the aggregate marks of that examination or up to 10 marks, whichever is less, shall be given the required marks to get the next higher class or grade as the case may be.

Provided that benefit of the above mentioned grace marks shall not be given, if the candidate fails to secure necessary passing marks in the aggregate head of passing also, if prescribed in the examination concerned.

Provided further that this grading is concurrent with the rules and guidelines of professional statutory bodies at the All India level such as AICTE, MCI, Bar Council, CCIM, CCIH, NCTE etc.
taking into consideration the report of the committee shall pass such orders as it deem fit including granting the student benefit of doubt, issuing warning or exonerating him/her from the charges and shall impose any one or more of the following punishment on the student/s found guilty of using unfair means:

(a) Annulling the performance of the student in full or in part in the examination he/she has appeared for.

(b) Debarring student from appearing for any examination of the University or college Institution for a stipulated period not exceeding five year.

(c) Debarring student from appearing for any examination of the University or college Institution for a stipulated period not exceeding five year.

(d) Cancellation of the University or College or Institution scholarship/s or award/s prize or medal etc. awarded to him/her in that examination.

(e) In addition to the above mentioned punishment, the competent authority may impose a fine not exceeding Rs.300/- on the student declared guilty. If the student concerned fails to pay the fine within a stipulated period, the competent authority may impose on such a student additional punishment/penalty as it may deem fit.

(f) The student concerned be informed of the punishment finally imposed on him/her in writing by the competent authority or by the officer authorized by it in this behalf, under intimation to the College/Institution he/she belongs to.

(g) An appeal against the findings of the committee shall lie with the concerned competent authority whose decision shall be final and binding.

(h) An appeal made in writing within a period of 30 days from the date imposition of the punishment shall be considered by the competent authority on merit and shall be decided on the basis of the evidence available in the case and shall be heard in person in deserving cases, if the competent authority finds substance in the appeal, the competent authority shall supply a typed copy of the relevant extract of fact-finding report of the inquiry committee, as well as documents relied upon (if not strictly confidential). Decision in the appeal shall be informed to the student concerned accordingly.

(i) The court matters in respect of the unfair means cases should be dealt with by the respective competent authority.
(j) As far as possible the quantum of punishment should be as prescribed (Category-wise in Appendix-I)

APPENDIX-I
THE BROAD CATEGORIES OF UNFAIR MEANS ADOPTED BY STUDENTS AT THE UNIVERSITY/-College/Institution examination AND THE QUANTUM OF PUNISHMENT FOR EACH CATEGORY THEREOF.

<table>
<thead>
<tr>
<th>Sr. No.</th>
<th>Nature of Malpractices</th>
<th>Quantum of Punishment</th>
</tr>
</thead>
<tbody>
<tr>
<td>1.</td>
<td>Possession of copying material</td>
<td>(Note:- This quantum of punishment shall apply also to the following categories of</td>
</tr>
<tr>
<td></td>
<td></td>
<td>malpractices at Sr. No. 2, to Sr. No. 12 in addition to the Punishment prescribed</td>
</tr>
<tr>
<td></td>
<td></td>
<td>thereat)</td>
</tr>
<tr>
<td>2.</td>
<td>Actual copying from the copying material</td>
<td>Exclusion of the student from university or College or Institution examination for one</td>
</tr>
<tr>
<td></td>
<td></td>
<td>additional examination.</td>
</tr>
<tr>
<td>3.</td>
<td>Possession of another students Answer Book</td>
<td>Exclusion of the student from University or College or Institution examination for one</td>
</tr>
<tr>
<td></td>
<td></td>
<td>additional examination (Both the students)</td>
</tr>
<tr>
<td>4.</td>
<td>Possession of another students Answer book+ actual evidence of Copying</td>
<td>Exclusion of the student from University or College or Institution examination for two additional examination (Both the Students)</td>
</tr>
<tr>
<td>5.</td>
<td>Mutual / Mass copying.</td>
<td>Exclusion of the student from University or College or Institution examination for two additional examinations.</td>
</tr>
<tr>
<td>6 (a)</td>
<td>Smuggling out or smuggling in of Answer book as copying material</td>
<td>Exclusion of the student from University or College or Institution examination for two additional examinations.</td>
</tr>
<tr>
<td>(b)</td>
<td>Smuggling in of written answer book based on the question paper set at the examination</td>
<td>Exclusion of the student from University or College or Institution examination for three additional examinations.</td>
</tr>
<tr>
<td>(c)</td>
<td>Smuggling in of written answer book and forging signature of Jr, Supervisor thereon</td>
<td>Exclusion of the student from University or College or Institution. Examination for four additional examinations.</td>
</tr>
<tr>
<td>CODE</td>
<td>Subject</td>
<td>Contact hr/week</td>
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<td>---------------------------------------------------</td>
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<tr>
<td>BSH/201</td>
<td>Mathematics-III</td>
<td>4</td>
</tr>
<tr>
<td>EED/202</td>
<td>Transformers and DC machines</td>
<td>4</td>
</tr>
<tr>
<td>EED/203</td>
<td>Electrical Measuring techniques</td>
<td>4</td>
</tr>
<tr>
<td>EED/204</td>
<td>Electrical power Generation and its Economics</td>
<td>4</td>
</tr>
<tr>
<td>EED/205</td>
<td>Electrical Engineering Materials</td>
<td>4</td>
</tr>
<tr>
<td>*EED/206</td>
<td>Electronic Devices and Circuits</td>
<td>4</td>
</tr>
<tr>
<td>EED/221</td>
<td>LAB-I: Transformers and DC machines</td>
<td>-</td>
</tr>
<tr>
<td>EED/222</td>
<td>LAB-II: Electrical Measuring techniques</td>
<td>-</td>
</tr>
<tr>
<td>EED/223</td>
<td>LAB-III: Electrical power Generation and its Economics</td>
<td>-</td>
</tr>
<tr>
<td>EED/224</td>
<td>LAB-IV: Electrical Engineering Materials</td>
<td>-</td>
</tr>
<tr>
<td>*EED/225</td>
<td>LAB V: Electronic Devices and Circuits</td>
<td>-</td>
</tr>
<tr>
<td>EED/226</td>
<td>LAB-VI Fundamentals of PLC</td>
<td>-</td>
</tr>
<tr>
<td>Total</td>
<td></td>
<td>20</td>
</tr>
</tbody>
</table>


*Electronic Devices and Circuits (EDC) subject only for Electrical Electronics Engineering Branch
<table>
<thead>
<tr>
<th>CODE</th>
<th>Subject</th>
<th>Contact hr/week</th>
<th>Examination scheme</th>
</tr>
</thead>
<tbody>
<tr>
<td>BSH/252</td>
<td>Mathematics-IV</td>
<td>L  T  P  TOTAL</td>
<td>CT    TH    TW    P  TOTAL</td>
</tr>
<tr>
<td>EED/253</td>
<td>AC machines</td>
<td>4 - - 4</td>
<td>20  80  - -</td>
</tr>
<tr>
<td>EED/254</td>
<td>Network Analysis</td>
<td>4 - - 4</td>
<td>20  80  - -</td>
</tr>
<tr>
<td>EED/255</td>
<td>Electrical Power Transmission and Distribution</td>
<td>4 - - 4</td>
<td>20  80  - -</td>
</tr>
<tr>
<td>EED/256</td>
<td>Analog and Digital circuits</td>
<td>4 - - 4</td>
<td>20  80  - -</td>
</tr>
<tr>
<td>EED/271</td>
<td>LAB-VII: AC machines</td>
<td>- - 2 2</td>
<td>- - 50  - -</td>
</tr>
<tr>
<td>EED/272</td>
<td>LAB-VIII: Network Analysis</td>
<td>2 - 2</td>
<td>- - 50  - -</td>
</tr>
<tr>
<td>EED/273</td>
<td>LAB-IX: Electrical Power transmission and distribution</td>
<td>2 - 2</td>
<td>- - 50  - -</td>
</tr>
<tr>
<td>EED/274</td>
<td>LAB-X: Analog and Digital Circuits</td>
<td>2 - 2</td>
<td>- - 50  - -</td>
</tr>
<tr>
<td>EED/275</td>
<td>LAB-XI: Communication Skill</td>
<td>2 - 2</td>
<td>- - 50  - -</td>
</tr>
<tr>
<td><strong>Total</strong></td>
<td><strong>Total</strong></td>
<td><strong>20 2 8 30</strong></td>
<td><strong>100 400 100 150 750</strong></td>
</tr>
</tbody>
</table>

L: Lecture  T: Tutorial  P: Practical  CT: Class Test  TH: Theory  TW: Term work  P/O: Practical / Oral
R3H 301 : Engineering Mathematics-III

SE (ALL)

Teaching Scheme
Theory: 4 Hrs/week

Examination scheme
Theory: 80 Marks (3 Hrs)
Class Test: 20 marks

Objectives: 1) To develop logical understanding of the subject
2) To develop mathematical skill so that students are able to apply mathematical methods & Principal’s in solving problems from Engineering fields
3) To produce graduates with mathematical knowledge & computational skill.

Unit 1: Linear Differential Equations : Linear Differential Equations with constant coefficients General method, shortcut methods to find particular integral, Homogenous Linear differential equations (Cauchy’s & Legendre’s form), method of variation of parameters. (6Hrs)

Unit 2: Application of LDE: To Electrical circuits & to Mechanical system (Analogous study of two systems), To Civil Engineering, Free oscillations / vibrations, Forced oscillation /vibrations, Damped Free oscillations / vibrations, Damped Forced oscillations / vibrations. (6Hrs)


Unit 4: Vector Differentiation: Differentiation of vectors, Gradient of scalar point function, Directional derivative, Divergence of vector point function, Curl of a vector point function. Irrotational and solenoidal vector field. (6Hrs)

Unit 5: Vector Calculus (Integral calculus): The line integral, Surface integral, volume integral, Gauss Divergence theorem, Stoke’s theorem, Green’s theorem (6Hrs)

Note: All Theorems are without proofs
Section A: Unit 1, 2, 3
Section B: Unit 4, 5, 6

Reference Books:

Pattern of Question Paper:
The units in the syllabus shall be divided in two equal sections. Question paper shall be set having two sections A and B. Section A questions shall be set on first three units (1,2,3) and Section B questions on remaining three units (4,5,6) . Question paper should cover the entire syllabus.

For 80 marks Paper:
1. Minimum ten questions
2. Five questions in each section
3. Question no 1 and 6 be made compulsory and should have at least ten bits of two marks out of which FIVE to be solved.
4. Two questions from remaining questions from each section be asked to solve having weight age of 15 marks
## Transformers and DC Machines

**Examination Scheme**
- Class Test: 20 Marks
- Theory Exam: 80 marks
- Theory Exam (duration): 3 hrs

<table>
<thead>
<tr>
<th>UNIT</th>
<th>CONTENT</th>
<th>HRS</th>
</tr>
</thead>
</table>
| 01   | Single phase Transformers  
Working of transformer on-load and off no-load, phasor diagrams Exact and approximate equivalent circuits referred to either side, losses, Efficiency, maximum efficiency, ratings. Open circuit and short circuit tests, determination of equivalent circuit parameters from the test data, Polarity test, Parallel operation, conditions to be satisfied, load sharing under various conditions.
Autotransformers, their ratings and applications. Comparison with two winding transformer with respect to saving of copper and size. | (10) |
| 02   | Three phase transformers  
Types, construction, comparison with a bank of three single phase transformers, Standard connections, phasor groups as per clock notations, and their suitability for particular applications, Polarity test, Efficiency & regulation by direct and indirect methods, Descriptive treatment of Parallel operation of three phase transformers Scott connection and 'V'-connections, three winding transformers, tertiary windings. | (8) |
| 03   | D.C. Machine construction  
Construction, main parts, magnetic circuit, typical flux path, Armature winding: Simple lap and wave winding, commutator and brush assembly. | (4) |
| 04   | DC Generator and DC motor action  
Generator and motor action, e.m.f equation, types, characteristics, applications, torque equation of motor, significance of back e.m.f. Working at no-load and on-load. Power flow diagram, losses and efficiency. Descriptive treatment of armature reaction. Commutation, causes of bad commutation and remedies, interpole, compensating windings (descriptive treatment only) | (8) |
| 05   | Starting, control and testing of DC motor:  
Starting of DC motors, starters for series and shunt motor, solid state starters, speed control, tests, Applications. | (6) |
| 06   | Special Purpose DC machines  
Construction and operating principles of Brush less DC motor, stepper motor, DC servo motor, PMDC motor. | (4) |

### Text Books:
1. Electrical Technology by Edward Hughes ELBS, Pearson Education.
2. Electrical Technology Vol II by B. L. Theraja
Reference Books:
3. Theory and Performance of DC machines by A.S. Langsdorf (Tata McGraw Hill)
5. Performance and Design of AC Machines by M.G. Say (CBS Publishers and Distributors)
6. Electrical Machines by Smarajit Ghosh (Pearson Education), New Delhi.

Pattern of Question Paper:
Six units in the syllabus shall be divided into two equal parts i.e., 3 units in each part. Question paper shall be set having two sections A and B. Section A questions shall be set on first part and Section B questions on second part. Question paper should cover the entire syllabus.

For 80 marks Paper:
1. Minimum ten questions
2. Five questions in each section
3. Question no 1 from section A and Question no 6 from section B having weightage of 10 marks each be made compulsory and should have at least eight bits of two marks out of which five to be solved.
4. Two questions from remaining questions from each section A and B be asked to solve each having weightage of 15 marks.
## Code: EED/203  Electrical Measurement Techniques

*(COMMON TO EEP/EE/EEE BRANCHES)*

**Teaching Scheme**
- Theory: 4 Hrs / week

**Examination Scheme**
- Class Test: 20 Marks
- Theory Exam: 80 marks
- Theory Exam (duration): 3 hrs

<table>
<thead>
<tr>
<th>UNIT</th>
<th>CONTENT</th>
<th>HRS</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Unit 01</strong></td>
<td>A) <strong>Measurement and Instrumentation theory:</strong> Characteristics of measuring instruments: Static and dynamic, accuracy, linearity, speed of response, dead zone, repeatability, resolution, span, reproducibility, drifts. Need for calibration, standards and their classification. Block diagram of generalized instrumentation system. Classification of measuring instruments - Absolute and secondary instruments, types of secondary instruments: indicating, integrating, and recording, analog / digital. <strong>B) Essentials of indicating instruments:</strong> deflecting, controlling and damping systems. Construction, working, torque equation, various advantages and disadvantages of MI (attraction and repulsion), and PMMC. <strong>C) Ammeter and Voltmeter theory:</strong> Extension of range of ammeters and voltmeters using shunt, multiplier. Universal shunt, Universal multiplier. Block diagram and operation of digital ammeters and voltmeters in brief.</td>
<td>(8)</td>
</tr>
<tr>
<td><strong>Unit 02</strong></td>
<td>A) <strong>Measurement of Resistance:</strong> Measurement of low, medium and high resistance. Kelvin's Double Bridge, Ammeter-Voltmeter method, Megger, Earth tester for earth resistance measurement, measurement of insulation resistance when power is ON. <strong>B) A.C. Bridges:</strong> Introduction, sources &amp; detectors for a.c. bridge, general equation for bridge at balance. Measurement of Inductance: Maxwell's Inductance &amp; Maxwell's Inductance - Capacitance Bridge, Andersons Bridge. Measurement of Capacitance: Shearing Bridge.</td>
<td>(4)</td>
</tr>
<tr>
<td><strong>Unit 03</strong></td>
<td>03) <strong>Wattmeter theory and measurement of power:</strong> Construction, working, torque equation, errors and their compensation, advantages/disadvantages of dynamometer type wattmeter, low power factor wattmeter, poly-phase wattmeter. Power measurement in three phase system. Power measurement in three phase system for balanced and unbalanced load using three wattmeter method, two wattmeter method.</td>
<td>(6)</td>
</tr>
<tr>
<td><strong>Unit 04</strong></td>
<td><strong>Energy meter theory:</strong> Construction, working, torque equation, errors and adjustments of single phase conventional (induction type) energy meter, Block diagram and operation of electronic energy meter. Three-phase energy meters.</td>
<td>(4)</td>
</tr>
<tr>
<td><strong>Unit 05</strong></td>
<td><strong>Instrument Transformers:</strong> Construction, connection of CT &amp; PT in the circuit, advantages of CT / PT over shunt and multipliers for range extension, transformation ratio, turns ratio, nominal ratio, burden etc, ratio and phase angle error. (No derivation of formulae is expected)</td>
<td>(3)</td>
</tr>
<tr>
<td><strong>Unit 06</strong></td>
<td>A) <strong>Oscilloscope:</strong> Introduction, various parts, front panel controls, block diagram of dual trace and dual beam CRO, use of CRO for measurement of voltage, period, frequency, phase angle &amp; frequency by lissajous pattern. <strong>B) Transducers:</strong> Introduction, classification, basic requirements, types: Resistive, inductive, Capacitive (brief treatment only), advantages of electrical transducers.</td>
<td>(3)</td>
</tr>
</tbody>
</table>
Text Books:
1. A Course in Electrical and Electronic measurements & Instrumentation – by A. K. Sawhaney, Dhanpat Rai & Sons

Reference Books:
2. Electronic measurement and instrumentation by Dr. Rajendra Prasad, Khanna Publisher, New Delhi.
4. Introduction to Measurements and instrumentation by Anand PHI Publication.

PATTERN OF QUESTION PAPER:
Six units in the syllabus shall be divided in two equal parts i.e. 3 units in each part. Question paper shall be set having two sections A and B. Section A questions shall be set on first part and Section B questions on second part. Question paper should cover the entire syllabus.

For 80 marks Paper:
1. Minimum ten questions
2. Five questions in each section
3. Question no 1 from section A and Question no 6 from section B having weightage of 10 marks each be made compulsory and should have at least eight bits of two marks out of which five to be solved.
4. Two questions from remaining questions from each section A and B be asked to solve each having weightage of 15 marks.
## CODE: EED/204  Electrical Power Generation and its Economics

(COMMON TO EE/EE/EEE BRANCHES)

**Teaching Scheme**
Theory: 4 Hrs / week

**Examination Scheme**
Class Test: 20 Marks
Theory Exam: 80 marks
Theory Exam (duration): 3 hrs

<table>
<thead>
<tr>
<th>UNIT</th>
<th>CONTENT</th>
<th>HRS</th>
</tr>
</thead>
<tbody>
<tr>
<td>Unit 01</td>
<td><strong>Thermal Power Plants:</strong> Types of boilers, Feed water and its treatment, Steam turbine and alternators. Site selection, Main parts and its working. Fuel Handling: delivery of load, unloading, preparation, transfer, outdoor (dead) storage, indoor (live) storage, In plant Handling, Coal weighing, Ash disposal and dust collation: Draught systems, electrostatic precipitator Prospectus and development of thermal plants in India</td>
<td>(8)</td>
</tr>
<tr>
<td>Unit 02</td>
<td><strong>Hydro Power Plant:</strong> Site selection, Hydrology, storage and pondage, general arrangements and operation of hydro power plant, Hydraulic turbines, turbine size, pelton wheel turbine, Francis and Kaplan turbines, selection of turbines, Dams, Spillways, gates, intake and out take works, canals and layout of penstocks, water hammer and surge tank, simple numerical on hydrographs and number of turbine required Prospectus and development of hydro plants in India</td>
<td>(8)</td>
</tr>
<tr>
<td>Unit 03</td>
<td><strong>Nuclear power plant:</strong> Introduction, atomic physics, nuclear reaction, materials, site selection, nuclear reactors and working of each part, classification of nuclear reactor, nuclear waste disposal, plant layout, Prospectus and development of nuclear plants in India Diesel Power Plants: Introduction, Site selection, Main components and its working, Diesel plant efficiency and heat balance, choice and characteristic of diesel power plant.</td>
<td>(8)</td>
</tr>
<tr>
<td>Unit 04</td>
<td><strong>Gas power plant:</strong> Simple gas turbine power plant, methods to improve thermal efficiency, open loop and closed loop cycle power plants, gas fuels, gas turbine materials, plant layout.</td>
<td>(3)</td>
</tr>
<tr>
<td>Unit 05</td>
<td><strong>Non-conventional power plant:</strong> Sources, MHD plants, solar energy, fuel cells, tidal power generation, geothermal power generation , wind power stations, Prospectus and development of non conventional power plants in India Comparison of all power plants.</td>
<td>(5)</td>
</tr>
<tr>
<td>Unit 06</td>
<td><strong>Economics Aspects of Power Generation:</strong> Introduction, terms commonly used in system operations, factors affecting cost of generation, reduction of cost by interconnecting generators, choice of size and number of generator units, Input output curves of thermal and hydropower plants, Incremental fuel rate curves, incremental fuel cost curve, constraints on economic generation, economic loading of generators, load allocation among various generators, base load and peak load plants.</td>
<td>(8)</td>
</tr>
</tbody>
</table>

**Text Books**
2. Dr. P. C. Sharma: Power Plant Engineering ,
3. Chakrabarti, Soni, Gupta, Bhatnagar “A text book on power system Engineering” Dhanpat Rai publication
4. R.K.Rajput, “Power Plant Engineering”
5. J B Gupta, , “Power Plant Engineering”

**Reference Books**
1. Arora and Domkundwar: A course in Power Plant Engineering , Dhapat Rai publication
2. S. P. Sukhatme: Solar Energy

PATTERN OF QUESTION PAPER:
Six units in the syllabus shall be divided in two equal parts i.e. 3 units in each part. Question paper shall be set having two sections A and B. Section A questions shall be set on first part and Section B questions on second part. Question paper should cover the entire syllabus.

For 80 marks Paper:
1. Minimum ten questions
2. Five questions in each section
3. Question no 1 from section A and Question no 6 from section B having weightage of 10 marks each be made compulsory and should have at least eight bits of two marks out of which five to be solved.
4. Two questions from remaining questions from each section A and B be asked to solve each having weightage of 15 marks.
## Code: EED/205 Electrical Engineering Materials

**Examination Scheme**
- Class Test: 20 Marks
- Theory Exam: 80 Marks
- Theory Exam (duration): 3 hrs

<table>
<thead>
<tr>
<th>UNIT</th>
<th>CONTENT</th>
</tr>
</thead>
</table>
| 01    | A) Dielectric Properties of Insulating Materials: Static Field, Dielectric Parameters [Dielectric constant, Dipole moment, Polarization, Polarizability], Mechanisms of Polarization-Mechanical, Ionic and Orientational Polarization (Descriptive treatment only), Pyro-Electric & Ferro-Electric Materials, Dielectric Loss and Loss Tangent.
  B) Dielectric Breakdown: Introduction, Concept of Primary & Secondary Ionization of Gases (Descriptive treatment only), Breakdown Voltage, Breakdown Strength, Factors affecting Breakdown Strengths of Gaseous, Liquid and Solid Dielectric Materials, Breakdown in Vacuum. |
| 03    | Magnetic Materials:
| 04    | Conducting Materials:
| 05    | Nanotechnology:
| 06    | Testing of Materials:
  - Measurement of Tangent of Dielectric Loss Angle (tan δ) by Schering Bridge-1S 13585-1994
  - Measurement of Dielectric Strength of Solid Insulating Material-1S 2584
5. Measurement of P.F. and partial discharge of high voltage cables.
6. Testing of high voltage bushing.
7. Measurement of Flux Density by Gauss-meter

Text Books:
2. Electrical Engineering Materials, T.T.I, Madras
4. Nanotechnology - A gentle introduction to next big idea by Mark Ratner & Daniel Ratner, Pearson Education
5. Introduction to Nanotechnology by Charles P. Poole, Jr. Frank & J. Ownes (Wiley Student Edition)
6. Introduction to Nano Science & Technology – Chattopadhyya

PATTERN OF QUESTION PAPER:
Six units in the syllabus shall be divided in two equal parts i.e. 3 units in each part. Question paper shall be set having two sections A and B. Section A questions shall be set on first part and Section B questions on second part. Question paper should cover the entire syllabus.

For 80 marks Paper:
1. Minimum ten questions
2. Five questions in each section
3. Question no 1 from section A and Question no 6 from section B having weightage of 10 marks each be made compulsory and should have at least eight bits of two marks out of which five to be solved.
4. Two questions from remaining questions from each section A and B be asked to solve each having weightage of 15 marks.
# Electronics Devices and Circuits

(ONLY for Electrical Electronics Engineering branch)

## Teaching Scheme
Theory: 4 Hrs / week

## Examination Scheme
Class Test: 20 Marks
Theory Exam: 80 marks
Theory Exam (duration): 3 hrs

<table>
<thead>
<tr>
<th>Unit</th>
<th>Content</th>
<th>Hrs</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>Rectifiers and Filters</td>
<td></td>
</tr>
<tr>
<td></td>
<td>Half wave rectifier, full wave rectifier, bridge rectifier, PIV, efficiency, ripple factor, TUF analysis, Ripple Factor calculation for C, LLC, voltage multiplier, with diodes, regulation with and without filter, load and line regulation.</td>
<td>5</td>
</tr>
<tr>
<td>2</td>
<td>Theory of Junction Transistors and field Effect Transistor</td>
<td></td>
</tr>
<tr>
<td></td>
<td>Transistor action – Transistor current components, Transistor as an amplifier and as switch, collector efficiency, early effect in transistor, Junction FET operation, Enhancement MOSFET, depletion MOSFET, comparison, JFET and MOSFET, Power MOSFET, Equivalent circuit operation, static characteristics – Transistor testing by different methods.</td>
<td>8</td>
</tr>
<tr>
<td>3</td>
<td>Bias Stability and Device Stabilization</td>
<td></td>
</tr>
<tr>
<td></td>
<td>Transistor Biasing: Location of Q point, fixed bias circuit, collector to base circuit, self bias circuit, graphical DC bias analysis, design of all biasing circuits, FET biasing: self biasing, voltage feed back biasing.</td>
<td>5</td>
</tr>
<tr>
<td>4</td>
<td>Amplifiers</td>
<td></td>
</tr>
<tr>
<td></td>
<td>Frequency Response, RC coupled and transformer coupled amplifier, single stage and multi stage amplifier, wide band amplifier, cascade amplifiers, feed back amplifiers, positive and negative feed back, current and voltage feed back. Effect of feedback on gain, Input and output impedance, Noise and distortion, (derivation treatment.) DC amplifiers: Drift in amplifiers, differential amplifiers,</td>
<td>6</td>
</tr>
<tr>
<td>5</td>
<td>Transistor Model</td>
<td></td>
</tr>
<tr>
<td></td>
<td>Hybrid parameters: H equivalent, Pi equivalent circuit, small signal single stage amplifier, analysis of CE, CC, CB circuits, voltage gain, current gain, input/output impedance, dependence on source and load impedance, emitter follower analysis, boot strapping in emitter follower.</td>
<td>4</td>
</tr>
</tbody>
</table>

### A. High Frequency Amplifiers

- High frequency equivalent circuits, for BJT and FET amplifiers, hybrid Pi equivalent circuit, Determination of lower and higher cutoff, frequencies, Effect of junction capacitance and miller’s theorem

### B. Power Amplifiers

- Types: Class A, Class B, Class AB, and Class C, capacitor coupled quasi complimentary, push pull, expression for efficiency of Class A class B amplifier, distortion in amplifiers

### C. Oscillators and Tuned Amplifiers

- Barkhausen criterion, RC and LC oscillators, Crystal oscillators, designing examples, Tuned Amplifiers – Single tuned, double tuned Stager tuned

### Reference books:


PATTERN OF QUESTION PAPER:
Six units in the syllabus shall be divided in two equal parts i.e. 3 units in each part. Question paper shall be set having two sections A and B. Section A questions shall be set on first part and Section B questions on second part. Question paper should cover the entire syllabus.

For 80 marks Paper:
1. Minimum ten questions
2. Five questions in each section
3. Question no 1 from section A and Question no 6 from section B having weightage of 10 marks each be made compulsory and should have at least eight bits of two marks out of which five to be solved.
4. Two questions from remaining questions from each section A and B be asked to solve each having weightage of 15 marks.
CODE: EED/221  
LAB-I TRANSFORMERS AND DC MACHINES
(COMMON TO EEP/EEE/EEE BRANCHES)

Teaching Scheme
Practical: 2 Hrs / week

Examination Scheme
Practical oral: 50 marks

Term work shall consist of: Any three experiments on transformer, four on D.C. machine and one on special purpose DC motor.

1. Internal, External & Magnetizing Characteristics of DC shunt & Series Generator.
2. Load Characteristics of DC Compound Generator
3. Load test on D.C. shunt motor
4. Speed control of D.C. Shunt motor, above and below rated speed.
5. Efficiency and losses calculation of DC motor by Swinburne’s test, limitations of this test.
6. Polarity test and ratio test on three phase transformer.
7. Parallel operation of single phase transformer.
8. Performing different 3-phase transformer connections.
9. Efficiency and regulation of three phase transformer by direct loading.
10. Efficiency and regulation of three phase transformer by indirect loading.
11. Working test on special purpose motors.
12. Swmpner’s test on 1-phase transformers.
13. Scott connection of single phase transformers.

CODE: EED/222  
LAB-II Electrical Measurement Techniques
(COMMON TO EEP/EEE/EEE BRANCHES)

Teaching Scheme
Practical: 2 Hrs / week

Examination Scheme
Practical oral: 50 marks

List of Experiments:

The term work shall consist of any 8 experiments from the list

1. Measurement of power in three phase circuit using two wattmeter method (Balanced & Unbalanced Loads)
2. Measurement of Reactive power in three phase balanced circuit using one wattmeter method and by one wattmeter method with two way switch.
3. Calibration of Single phase or Three phase static energy meter at different power factors using Digital meters.
6. Earth resistance measurement by Earth Tester.
7. Extension of instrument range: ammeter, voltmeter, watt meter using CT / PT.
8. Measurement of power in three phase four wire using three CTs and Two wattmeters.
10. Study of electrical transducers
CODE: EED/223  LAB-III Electrical Power Generation and its Economics  
(COMMON TO EEP/EE/EEE BRANCHES)  
Teaching Scheme  
Practical: 2 Hrs / week  

Examination Scheme  
Term work: 50 marks  

Practicals: The term work shall consist of a record of any FIVE of the following:  
PART-A  
1. Study of boiler mounting and accessories.  
2. Study of modern thermal power plant.  
3. Demonstration and study on diesel engine.  
4. Demonstration and study on diesel power plant.  
5. Study of modern hydroelectric power plant.  
6. Demonstration and study of solar photovoltaic system.  
7. Demonstration and study of any water turbine.  
8. Demonstration and study of a centrifugal pump.  
PART-B  
Arrange one industrial visit to any electrical power generating station and ask the students to submit the report.

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CODE: EED/224  LAB-IV Electrical Engineering Materials  
(COMMON TO EEP/EE/EEE BRANCHES)  
Teaching Scheme  
Practical: 2 Hrs / week

List of Experiments:  
At least two experiments should be designed by the faculty members and can be included in the term work apart from the experiment list given below. SIX experiments from the list below and remaining two from the experiments designed and set up by the faculty member will form part of term work.  
1. To measure electric strength of solid insulating materials as per IS 2584.  
2. To measure electric strength of liquid insulating materials as per IS 6798.  
3. To measure electric strength of gaseous insulating materials using Sphere Gap-Unit.  
4. To obtain Hysteresis Loop of the Ferro-Magnetic Material.  
5. To understand the principle of thermocouple & to obtain characteristics of different thermocouples.  
6. To measure Insulation Resistance & KVAR capacity of power capacitor.  
7. To measure Resistivity of High Resistive Alloys.  
8. To observe development of tracks due to ageing on different insulating materials e.g. Bakelite, Perspex, Mica, Micanite, Fiberglass etc.  
9. Testing of Cables as per IS 6380, 6474.  
10. Measurement of Tangent of Dielectric Loss Angle (tan δ) by Schering Bridge  
11. Measurement of Flux Density by Gauss-meter
CODE: CODE: *EED/225   LAB:-V Electronic Device and Circuits
(ONLY for Electrical Electronics Engineering branch)
Teaching Scheme
Practical: 2 Hrs / week

Examination Scheme
Practical oral: 50 marks

The practical examination shall consists of performing an experiment on practical work done during the course, the record of the experiments submitted by the candidate and viva-voce based on the syllabus.
The assessment will be based on
1. Performing an experiment
2. Record of experiments submitted by the candidate
3. Viva-voce on syllabus

Minimum eight experiments should be conducted during the course and record (journal) for the same shall be submitted.
List of the Practicals:
1. Determination of ripple factor. PIV. Efficiency regulation factor of Half wave and full wave amplifiers (with and without capacitors).
2. Comparative study of fixed and self biased circuits.
3. Study of transformer coupled power amplifiers.
4. Study of transistor characteristics in CE configuration and Determination of h-parameters graphically.
5. Comparison of frequency response of RC coupled amplifiers with feedback and without feedback (comparison of Gain and Bandwidth).
6. Determination of Voltage gain, current gain, input and output impedance of FET amplifiers.
7. Designing and testing of RC phase shift oscillator.
8. Study and frequency calculation of Hartley oscillator.
9. Study and frequency calculation of Colpitts oscillator.
10. Study of class A, class B, class AB amplifiers.
**LAB- VI Fundamentals of PLC**

**Teaching Scheme**
Practical: 2 Hrs./Week

<table>
<thead>
<tr>
<th>UNIT</th>
<th>CONTENT</th>
</tr>
</thead>
<tbody>
<tr>
<td>Unit 01</td>
<td>modules (interfaces), power supplies, plc advantages &amp; disadvantages. selection criteria introduction to plc: definition &amp; history of plc, overall plc system, plc input &amp; output modules, central processing unit, cpu &amp; programmer/monitors, solid state memory, the processor for plc.</td>
</tr>
<tr>
<td>Unit 02</td>
<td>Programming of PLC: programming equipments, proper construction of plc ladder diagram, basic components &amp; their symbols in ladder diagram, fundamentals of ladder diagram, boolean logic &amp; relay logic, and analysis of rungs. input on/off switching devices, input analog devices, output on/off devices, output analog devices, programming on/off inputs to produce on/off outputs.</td>
</tr>
<tr>
<td>Unit 03</td>
<td>Advanced PLC Function: Analog PLC operation, PID control of continuous processes, simple closed loop systems, problems with simple closed loop systems, closed loop system using Proportional, Integral &amp; Derivative (PID), PLC interface, and industrial process example.</td>
</tr>
</tbody>
</table>

**Text Books:**
1) Gary Dunning, "Introduction to Programmable Logic Controllers", Thomson, 2nd Edition
2) John R. Hackworth, Frederick D., Hackworth Jr., "Programmable Logic Controllers Programming Methods and Applications"

**Reference Books:**
2) Bennett Stuart, "Real Time Computer Control", Prentice Hall, 1988
4) P. K. Srivstava, "Programmable Logic Controllers with Applications", BPB Publications

**List of Experiments:**

**PART-A**
1) Interfacing of lamp & button with PLC for ON & OFF operation.
2) Performed delayed operation of lamp by using push button.
3) Multiple push button operation with delayed lamp for ON/OFF operation.
4) Combination of counter & timer for lamp ON/OFF operation.
5) Set / Reset operation: one push button for ON & other push button for OFF operation.
6) DOL starter & star delta starter operation by using PLC.
7) PLC based temperature sensing using RTD.
8) PLC based thermal ON/OFF control.
9) Interfacing of Encoder with PLC (Incremental/Decremental)
10) PLC based speed, position measurement system.

**PART-B**
Submit a mini project based on above syllabus in the group of 4-5 students.
BSH251: Engineering Mathematics-IV

Teaching Scheme
Theory: 4 Hrs/week

Examination scheme
Theory: 80 Marks (3 Hrs)
Class Test: 20 marks

Objectives:
1) To develop Logical understanding of the subject
2) To develop mathematical skill so that students are able to apply mathematical methods & Principal's in solving problems from Engineering fields
3) To produce graduates with mathematical knowledge & computational skill.

Unit 1: Function of complex variable (Differential calculus)
Introduction, Analytic function Cauchy Riemann equations in Cartesian and Polar form, Harmonic function, Taylor’s series & Laurent’s series (without proof), Conformal mapping (geometrical representation of function of complex variable), bilinear transformation. (7Hrs)

Unit 2: Function of complex variable: (Integral calculus):
Line integral, contour integral Cauchy’s integral theorem, Cauchy’s integral formula (without proof), Residues, Cauchy’s residue theorem, Integration along unit circle and along upper half of semi circle. (7Hrs)

Unit 3: Application of PDE
Solutions of partial differential equation by method of separations of variables, Application to vibration of string, one dimensional heat flow equations, Laplace equation in two dimensions with boundary conditions. (6Hrs)

Unit 4: Laplace transform
Definition, Transforms of elementary functions, Properties & theorems of Laplace transforms(without proof), transforms of periodic function, Heaviside unit step function, displaced unit step function, Dirac delta function, error function, Bessel’ function of zero order. (6Hrs)

Unit 5: Inverse Laplace transform and its applications
Inverse Laplace transforms by using i) properties, ii) partial fractions, iii) Convolution theorem, Applications to solve linear differential equations with constant coefficients (Initial value problems), Simultaneous Linear differential equations. (6Hrs)
Unit 6: Fourier Transform and its applications
Fourier integral, Fourier sine and cosine integral, complex form of Fourier integral, Fourier transforms Fourier sine and cosine transform and inverse Fourier transforms Finite Fourier sine and cosine transforms. Solution of one dimensional heat equation by using Fourier transform.

Note: All Theorems are without proofs
Section A: Unit 1, 2, 3
Section B: Unit 4, 5, 6

Reference Books:

Pattern of Question Paper:
The units in the syllabus shall be divided in two equal sections. Question paper shall be set having two sections A and B. Section A questions shall be set on first three units (1,2,3) and Section B questions on remaining three units (4,5,6). Question paper should cover the entire syllabus.

For 80 marks Paper:
1. Minimum ten questions
2. Five questions in each section
3. Question no 1 and 6 be made compulsory and should have at least ten bits of two marks out of which FIVE to be solved.
4. Two questions from remaining questions from each section be asked to solve having weight age of 15 marks.
### A.C. MACHINES

- **Examination Scheme**
  - Class Test: 20 Marks
  - Theory Exam: 80 marks
  - Theory Exam (duration): 3 hrs

<table>
<thead>
<tr>
<th>UNIT</th>
<th>CONTENT</th>
<th>HRS</th>
</tr>
</thead>
<tbody>
<tr>
<td>UNIT 01</td>
<td><strong>THREE PHASE INDUCTION MOTOR</strong>: Construction, Principle of operation, torque equation and torque ratios, speed equation, speed torque characteristics, Effect of increase in rotor resistance, phasor diagram, equivalent circuit, analysis based on approximate equivalent circuit, no load test, blocked rotor test, efficiency and losses calculations, induction generator. Double Cage Induction Motor (D.C.I.M.): Construction, Characteristics and Equivalent circuit and applications. Speed control of Induction Motor : starting and types of starters, Speed control methods, Change of supply frequency, pole changing, cascading, Injection of EMF in secondary.</td>
<td>13</td>
</tr>
<tr>
<td>UNIT 02</td>
<td><strong>SINGLE PHASE INDUCTION MOTOR</strong>: Types, Construction, Principles of operation (capacitor start induction run, capacitor start capacitor run, inductor start induction run), phasor diagram, equivalent circuit, Experimental determination of parameter, applications.</td>
<td>05</td>
</tr>
<tr>
<td>UNIT 03</td>
<td>Special purpose machines: Construction and operating principles of AC servomotor, Repulsion motor, FHP synchronous Motor, and Hysteresis motor.</td>
<td>05</td>
</tr>
<tr>
<td>UNIT 04</td>
<td><strong>SYNCHRONOUS GENERATOR</strong>: Construction, Principles of operation, EMF equation, leakage reactance, armature reaction, armature resistance and reactance, field excitation system, damper winding.</td>
<td>05</td>
</tr>
<tr>
<td>UNIT 05</td>
<td><strong>PERFORMANCE CHARACTERISTICS OF SYNCHRONOUS GENERATOR</strong>: Calculation of voltage regulation by synchronous Impedance method, MMF method, Zero power factor method, experimental setup for above method, rating, efficiency and losses, method of synchronizing, synchronizing power, hunting, damping operation single and Infinite bus, power angle equation, short circuit ratio and its significance. Two reaction theory.</td>
<td>08</td>
</tr>
<tr>
<td>UNIT 06</td>
<td><strong>SYNCHRONOUS MOTOR</strong>: Method of starting, phaser diagram, torque and torque angle equation, V-curves and experimental setup, hunting and damping, synchronous condenser.</td>
<td>04</td>
</tr>
</tbody>
</table>

**REFERENCE BOOKS**:
2. Electrical Machines by Smarajit Ghosh (Pearson Education), New Delhi.
3. Performance and design of A.C.Machines – M.G.Say
5. Theory of A.C. Machines – Langes dorf
6. A.C. Machines -Puchstein Loyd and Conard.

**TEXT BOOKS**:
1. Electrical machines- Dr.P.S.Bhimra- Khanna Publication.

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For 80 marks Paper:
1. Minimum ten questions
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3. Question no 1 from section A and Question no 6 from section B having weightage of 10 marks each be made compulsory and should have at least eight bits of two marks out of which five to be solved.
4. Two questions from remaining questions from each section A and B be asked to solve each having weightage of 15 marks.
## NETWORK ANALYSIS

**Teaching Scheme**
- Theory: 4 Hrs / week

**Examination Scheme**
- Class Test: 20 Marks
- Theory Exam: 80 marks
- Theory Exam (duration): 3 hrs

<table>
<thead>
<tr>
<th>UNIT</th>
<th>CONTENT</th>
<th>HRS</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Unit 01</strong></td>
<td>Types of Networks: Lumped and distributed linear and nonlinear, bilateral and unilateral, time variant and time invariant, space variant and space invariant. Independent and dependent (controlled) voltage and current sources. Source transformation and shifting.</td>
<td>[02]</td>
</tr>
<tr>
<td><strong>B) Network Equations:</strong> Network equations on loop basis and node basis, choice between loop analysis and node analysis. Concept of super node and super mesh, concept of voltage and current divider, mutual inductance, dot convention for coupled circuits, Concept of duality and dual networks.</td>
<td>[04]</td>
<td></td>
</tr>
<tr>
<td><strong>Unit 02</strong></td>
<td>Superposition, Thevenin, Norton, Reciprocity, Substitution, Compensation, Millman's theorems applied to electrical networks with all types of sources.</td>
<td>[04]</td>
</tr>
<tr>
<td><strong>Unit 03</strong></td>
<td>Solutions of differential equations and network equations using Laplace transform method and classical method for R-L-R-C and R-L-C circuits (series and parallel), Inverse Laplace transforms, transformed networks with initial conditions. Analysis of electrical circuits with applications of step, pulse, impulse &amp; ramp functions, shifted &amp; singular functions the convolution integral. Laplace transforms various periodic and non periodic waveforms application of Laplace transforms.</td>
<td>[10]</td>
</tr>
<tr>
<td><strong>Unit 04</strong></td>
<td>Two Port Network: Z, Y, H and transmission parameters, inter-relations between parameters. Input power, Power transfer and Insertion loss: Energy and power, Effective or Root-Mean Square values, Average power and complex power, Problems in Optimizing power transfer, Insertion Loss</td>
<td>[03] [03]</td>
</tr>
<tr>
<td><strong>Unit 05</strong></td>
<td>Fourier Analysis: The Fourier series, Evaluation of Fourier coefficients, symmetry considerations, exponential form of Fourier series, steady state response to periodic signals.</td>
<td>[06]</td>
</tr>
<tr>
<td><strong>Unit 06</strong></td>
<td>Network Functions: Poles and Zeros, Terminal pairs or ports, network functions for the one port and two port. The calculation of network functions, ladder networks, general networks. Poles and zeros of network functions, Restrictions on poles and zeros locations for transfer functions, Time -domain behavior from the pole and zero plot. Stability of active networks.</td>
<td>[08]</td>
</tr>
</tbody>
</table>

**Text Books:**
5. Introduction to Electric Circuits – Alexander & Sadiku.
8. Electrical Circuit Analysis by P. Rameshbebu, Scitech PublicationIndia Pvt Ltd, Second Edition

**Reference Books:**
PATTERN PATTERN OF QUESTION PAPER:
Six units in the syllabus shall be divided into two equal parts i.e. 3 units in each part. Question paper shall be set having two sections A and B. Section A questions shall be set on first part and Section B questions on second part. Question paper should cover the entire syllabus.
For 80 marks Paper:
1. Minimum ten questions
2. Five questions in each section
3. Question no 1 from section A and Question no 6 from section B having weightage of 10 marks each be made compulsory and should have at least eight bits of two marks out of which five to be solved.
4. Two questions from remaining questions from each section A and B be asked to solve each having weightage of 15 marks.
<table>
<thead>
<tr>
<th>UNIT</th>
<th>CONTENT</th>
<th>HRS</th>
</tr>
</thead>
<tbody>
<tr>
<td>Unit 01</td>
<td>A) Different types of distribution systems, like ring main, and radial distribution, types &amp; choice of distribution system conductor, types of loads in transmission and distribution systems, Load curve, load duration curve, load factor, demand factor, diversity factor, load forecasting concept.</td>
<td>(04)</td>
</tr>
<tr>
<td></td>
<td><strong>B) Tariff:</strong> Residential, commercial, H.T., L.T. Time of Day tariff, Incentives and penalties.</td>
<td>(01)</td>
</tr>
<tr>
<td>Unit 02</td>
<td>A) Major Electrical equipments in Transmission Sub-Stations : Descriptive treatment of ratings, Special features, field of use of equipments like transformers, bus-bars, voltage regulators, switches and isolators, reactors, Control panels, metering, power supplies like station transformers, storage batteries and other control room equipments in sub-stations.</td>
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<tr>
<td></td>
<td><strong>B) Overhead line insulators:</strong> Types of insulators, pin type, suspension type, shackle type, strain type insulators, voltage distribution along string of suspension insulators, cripping distance of insulators string efficiency, Equalization of potential across each unit.</td>
<td>(03)</td>
</tr>
<tr>
<td>Unit 03</td>
<td><strong>Constants of Transmission Line:</strong> Inductance, Resistance of line, skin effect and its effects, proximity effect, inductance of single phase two wire line, flux linkage of one conductor of one group, inductance of composite conductor line, concept of G.M.R. and G.M.D., inductance of three phase line with equilateral spacing, inductance of parallel circuit three phase line, three phase line with equilateral spacing, unsymmetrical spacing, double circuit three phase line, Calculation of, inductance to be done with and without transposition.</td>
<td>(08)</td>
</tr>
<tr>
<td>Unit 04</td>
<td><strong>Constants of Transmission line:</strong> Capacitance: Concept of G.M.R. and G.M.D for capacitance calculations, capacitance of three phase line with equilateral spacing, capacitance of parallel circuit three phase line with equilateral spacing, unsymmetrical spacing, double circuit three phase line, capacitance of single phase line with earth effect and without effect of earth's surface on electric field, calculation of capacitance to be done with and without transposition.</td>
<td>(08)</td>
</tr>
<tr>
<td>Unit 05</td>
<td>A) Circuit Representation of Lines and generalized Circuit Constants : Classification of lines based on length as short, medium and long lines. Ferranti Effect Representation of lines as 'P/I' and 'Tee' circuits using R, L and C parameters voltage and current relations for short and medium lines only. Representation of 'Tee' and 'P/I' models of lines as two port networks, evaluation and estimation of ABCD constants for both the models.</td>
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<tr>
<td></td>
<td><strong>B) Long transmission line : Current and voltage relationship, Hyperbolic equations, Equivalence circuit</strong></td>
<td>(02)</td>
</tr>
<tr>
<td>Unit 06</td>
<td>A) Mechanical design of overhead lines : Line supports, spacing between the conductors, length of span, calculation of sag, equal and unequal supports, effect of ice and wind loadings. (02)</td>
<td></td>
</tr>
<tr>
<td></td>
<td><strong>B) Underground Cable : Classification, Construction of cable, XLPE cables, insulation resistance, capacitance, dielectric stress in single core/multi core cables, cable faults and location of faults.</strong></td>
<td>(02)</td>
</tr>
</tbody>
</table>

**Text Books:**


Reference Books:
1. Elements of Power Station Design by M.V. Deshpande, Wheeler Publishing.
5. Websites of MERC and MSEDCL

PATTERN OF QUESTION PAPER:
Six units in the syllabus shall be divided in two equal parts i.e. 3 units in each part. Question paper shall be set having two sections A and B. Section A questions shall be set on first part and Section B questions on second part. Question paper should cover the entire syllabus.

For 80 marks Paper:
1. Minimum ten questions
2. Five questions in each section
3. Question no 1 from section A and Question no 6 from section B having weightage of 10 marks each be made compulsory and should have at least eight bits of two marks out of which five to be solved.
4. Two questions from remaining questions from each section A and B be asked to solve each having weight age of 15 marks.

CODE: EED/256      Analog and Digital Circuits
(COMMON TO EEP/EE/EEE BRANCHES)
Teaching Scheme
Theory: 4 Hrs / week

Examination Scheme
Class Test: 20 Marks
Theory Exam: 80 marks
Theory Exam(duration): 3 hrs
<table>
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<tr>
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<tr>
<td>Unit 01</td>
<td>BJT: amplifier with reference to operational analysis of CE, CB and CC configuration, their input-output characteristics, AC-DC load line analysis, Class A, amplifier, Multistage BJT amplifier-direct, RC coupled and transformer coupled, Darlington pair, Push-Pull amplifier and differential amplifier FET-construction, Parameters, Characteristics.</td>
<td>(8)</td>
</tr>
<tr>
<td>Unit 02</td>
<td>Op-Amp : Block diagrams of 741 and 324 , ideal and practical parameters open loop and close loop configuration of Op-Amp. Applications of Op-Amp, Integrator, differentiator, Comparator, Schmitt trigger, instrumentation amplifier, precision rectifiers, zero crossing detectors, V-I and I-V converters.</td>
<td>(6)</td>
</tr>
<tr>
<td>Unit 03</td>
<td>Waveform generation using Op-amp - sine, square, saw tooth and triangular generator, peak detector, IC 555 -construction, working and modes of operation - astable, monostable and multivibrators, Sequence generator, voltage regulators using ICs Viz. 78xx, 79xx, LM 317, Active filters-its configuration with frequency response, Analysis of first order low pass and high pass filters.</td>
<td>(8)</td>
</tr>
<tr>
<td>Unit 04</td>
<td>Numbering Systems and Boolean algebra- numbering systems-binary, octal, decimal and hexadecimal and their conversion, codes-BCD, Grey and excess3, Binary arithmetic- addition and subtraction by 1's and 2's compliment. Revision of logic gates, Booleans algebra, De-morgan's theory etc. K-map: - structure for two, three and four variables, SOP and POS form reduction of Boolean expressions by K-map 1-bit comparator analysis using K-map</td>
<td>(8)</td>
</tr>
<tr>
<td>Unit 06</td>
<td>Multiplexer, Demultiplexer using K-map, ADC, Dual slope SAR, DAC-binary weighted, ladder type, Memories: RAM-static &amp; dynamic, ROM, PROMs and EPROMs , EEPROMS detailing.</td>
<td>(4)</td>
</tr>
</tbody>
</table>

**Text Books:**
3. Electronics Devices & Circuits by Mottershed, PHI New Delhi
6. Introduction to Electronics for Engineers and Scientists by Raja Raman, Vishwanathan and Mehata.

**References Books:**
1. Operational Amplifier by Gaikwad R. PHI New Delhi

**PATTERN OF QUESTION PAPER:**
Six units in the syllabus shall be divided in two equal parts i.e. 3 units in each part. Question paper shall be set having two sections A and B. Section A questions shall be set on first part and Section B questions on second part. Question paper should cover the entire syllabus.
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ODE: EED/271        LAB: VII AC MACHINES
(COMMON TO EEP/EE/EEE BRANCHES)          Examination Scheme
Teaching Scheme               Practical oral: 50 marks
Practical: 2 Hrs / week

TERM WORK : Minimum  Eight experiment based on above syllabus given in the list.

LIST OF EXPERIMENTS :
1. Study of A.C. Machines.
2. No load and Blocked rotor test on 3-PHASE induction-motor
3. Load test on 3-phase I.M.
4. Speed control of Induction Motor
5. Parameter calculation of single phase induction motor from No load and Blocked rotor test
7. Determination of voltage regulation of alternator by Direct loading method
Basics of Communication Skills

Teaching Scheme
Practical: 2 Hrs/Week

Examination Scheme
Term Work: 50 Marks

Course Curriculum

Unit I  Grammar and Usage  7 Hrs
- Overview of basic Mid-level English Grammar.
- Parts of Speech
- Prepositions and Conditionals.
- Tense and concept of time.
- Sentence Construction (Concord).
- Vocabulary: Words, Idioms, Phrases, Antonyms and Synonyms.

Unit II  Speaking Skills  5 Hrs
- Training in Sound Recognition
- Stress and Intonation pattern in spoken communication
- Rhythm and effective English communication
- Sound Recognition Exercise (Language Lab Exercise).
- Common Errors in English.

Unit III  Listening and Reading Skills  3 Hrs
- Active and Passive Listening
- Note taking tips
- Techniques of reading
- Types and Techniques – skimming and scanning of reading

Unit IV  Writing Skills  5 Hrs
- Identification of different writing styles (Four Writing Styles).
- Business Letters
- E-mail Writing
- Report Writing
- Job Applications
- Resume Preparation
- Drafting: Memo, Circulars, Notices, Agendas etc.

****
**Term Work:** The Term Work consists of 10 Experiments from the above said syllabus.

**Texts:**

**References:**

**Web Links:**
- [http://englishtrainer.blogspot.in](http://englishtrainer.blogspot.in)
- [http://www.englishclub.com/learn-english/language-skills.htm](http://www.englishclub.com/learn-english/language-skills.htm)
9. Regulation of alternator by slip test.
12. Reversal of Synchronous motor.

CODE: EED/272  LAB: VIII NETWORK ANALYSIS
(COMMON TO EEP/EE/EEE BRANCHES)
Teaching Scheme Examination Scheme
Practical: 2 Hrs / week Term Work: 50 marks

List of Practical:
Any four experiments from the first five of the following and any four experiments from rest of the list. (Minimum four experiments should be based on simulation software PSPICE/MATLAB along with hardware verification)

1. Verification of Superposition theorem in A.C. circuits.
2. Verification of Thevenin's theorem in A.C. circuits.
3. Verification of Reciprocity theorem in A.C. circuits.
4. Verification of Millman's theorem.
5. Determination of time response of R-C circuit to a step D.C. voltage input. (Charging and discharging of a capacitor through a resistor)
6. Determination of time response of R-L circuit to a step D.C. voltage input. (Rise and decay of current in an inductive circuit)
7. Determination of time response of R-L-C series circuit to a step D.C. voltage input.
8. Determination of parameter of two port network.
10. Determination of resonance, bandwidth and Q factor of R-L-C series circuit.
11. Determination of resonance of R-L-C Parallel circuit.
CODE: EED/273  LAB-IX Electrical Power Transmission and Distribution  
(COMMON TO EEP/EE/EEE BRANCHES)  
Teaching Scheme  
Practical: 2 Hrs / week  

Examination Scheme  
Practical oral: 50 marks  

PART-A  
Minimum Eight Experiments should be taken.  
1. Study of Transmission Sub-station & Drawing sheet of 132kV or 400kV sub-station.  
2. Study and Drawing sheet of pin type, strain type, 7 shackle type insulators.  
5. Meggering of armored cable with 2500V megger.  
7. Mat-lab simulation of transmission line model.  
8. Mat-lab simulation of faults in transmission line model.  
10. Evaluation of line parameters of 'n' model. (Expt. on model kit.)  
11. Evaluation of line parameters of 'T' model. (Expt. on model kit.)  

PART: B  
Visit to transmission station and submit the report individually.

CODE: EED/274  LAB-X ANALOG AND DIGITAL ELECTRONICS  
(COMMON TO EEP/EE/EEE BRANCHES)  
Teaching Scheme  
Practical: 2 Hrs / week  

Examination Scheme  
Practical oral: 50 marks  

Minimum 08 experiments to be conducted.  
1. Transistor amplifiers: frequency response of BJT, multistage BJT amplifier and FET amplifier.  
2. Op-amp as square, sine and triangular wave generator.  
3. Op-amp as ZCD, Comparator and Schmitt trigger.  
4. Instrumentation amplifier using 3-op amp CMR measurement and precision rectifier  
5. IC-555 applications- astable, monostable, sequence counter.  
6. Study and verify shift register operation (IC 7495) and application of 7495 as pseudo random no. generation  
7. Voltage regulation of IC VR 78xx, 79xx and LM317  
8. Study of counters, ring counter and twisted ring counter.  
9. A to D and D to A converter using ADC 0809 and DAC 0808.  
10. Study of up - down counters (IC 74192/74193) and N-modulo counter. (IC 7490/7493).  
11. Study of various flip-flops and verification of truth table.  
12. Study of Multiplexer and Demultiplexer.  
13. Study of active filters- Low pass and high pass filters.
Basics of Communication Skills

Course Curriculum

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- Prepositions and Conditionals.
- Tense and concept of time.
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- [http://www.englishclub.com/learn-english/language-skills.htm](http://www.englishclub.com/learn-english/language-skills.htm)